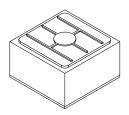
# ODH1212TF.A1

**OS-CORE®** ThinGaAIP



#### **Features:**

- Polarity: n-side up
- Chip technology: Thinfilm
- Color: hyper red
- Chipsize: 12 mil x 12 mil
- ESD: 2 kV acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 2)

#### **Ordering Information**

Type ODH1212TF.A1-MM-MM-1-C Ordering Code Q65112A1969



### **Maximum Ratings**

Parameter	Symbol		Values
Operating Temperature	T <sub>op</sub>	min.	-40 °C
		max.	110 °C
Storage Temperature <sup>1)</sup>	T <sub>stg</sub>	min.	-40 °C
		max.	110 °C
Recommended Die Storage Temperature ≤ 60% RH	$T_{stg die}$	max.	30 °C
Junction Temperature	T <sub>j</sub>	max.	125 °C
Junction temperature for short time applications*	T <sub>j</sub>	max.	150 °C
Forward Current	I <sub>F</sub>	min.	3 mA
T <sub>J</sub> = 25 °C	·	max.	100 mA
Forward Current Pulsed t $\leq$ 10 µs; D = 0.005 ; T <sub>J</sub> = 25 °C	I <sub>F pulse</sub>	max.	100 mA
Reverse voltage <sup>2)</sup> T <sub>J</sub> = 25 °C	V <sub>R</sub>	max.	12 V
ESD withstand voltage acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 2)	$V_{\text{ESD}}$		2 kV

\*The median lifetime (L70/B50) for Tj =150°C is 100h.

### **Characteristics**

 $I_{_{\rm F}}$  = 50 mA;  $T_{_{\rm J}}$  = 25 °C

Parameter	Symbol		Values
Centroid Wavelength <sup>3)</sup> I <sub>F</sub> = 50 mA	$\lambda_{centroid}$	min. max.	652 nm 663 nm
Forward Voltage <sup>4)</sup> I <sub>F</sub> = 50 mA	V <sub>F</sub>	min. typ. max.	1.90 V 2.30 V 2.40 V

### **Additional Information**

Die bonding	Metalization frontside	Metalization backside
Adhesive bonding	Gold	Gold



#### ODH1212TF.A1

## Binning Table 5)3)

#### I<sub>F</sub> = 50 mA Radiant Intensity Centroid Wavelength $\mathsf{I}_{\mathsf{e}}$ $\lambda_{\text{centroid}}$ a. u. nm 652 - 655 655 - 658 658 - 659 659 - 663 8.0 - 10.5 B10 C10 D10 A10 10.5 - 15.0 A13 B13 C13 D13

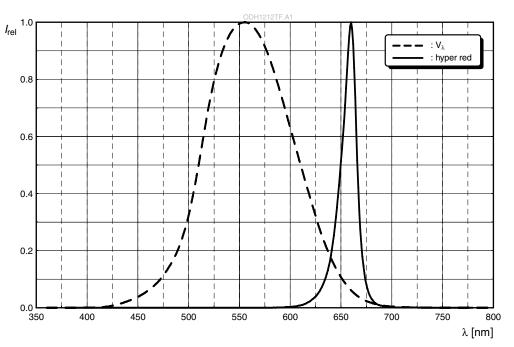
### Correlation factor <sup>6)</sup>

Unit	Value	Condition
CF (mW/sr / a.u.)	1.06	chip to air
CF (mW / a.u.)	4.8	chip with silicone lens



### **Relative Spectral Emission** <sup>7)</sup>

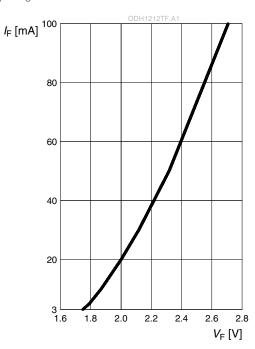
 $I_{rel}$  = f (λ);  $I_{F}$  = 50 mA;  $T_{S}$  = 25 °C





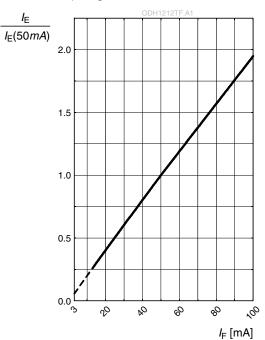
#### Forward current <sup>7), 8)</sup>

 $I_{_{\rm F}} = f(V_{_{\rm F}}); T_{_{\rm S}} = 25 \ ^{\circ}{\rm C}$ 



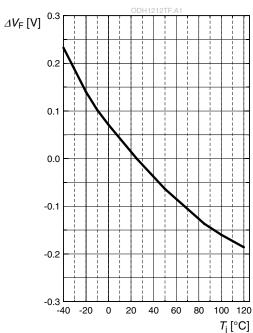
### Relative Radiant Intensity 7), 8)

 $I_{E}/I_{E}(50 \text{ mA}) = f(I_{F}); T_{S} = 25 \text{ °C}$ 



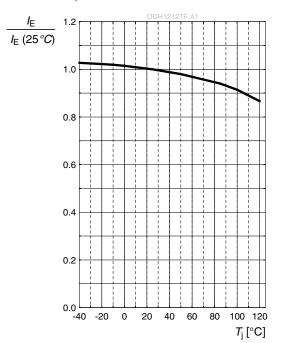


# Forward Voltage <sup>7)</sup> $\Delta V_F = V_F - V_F (25 \text{ °C}) = f(T_j); I_F = 50 \text{ mA}$

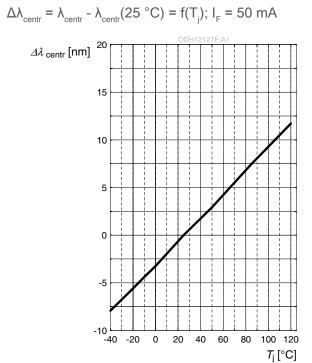


### **Relative Radiant Intensity** <sup>7)</sup>

 $I_{E}/I_{E}(25 \text{ °C}) = f(T_{i}); I_{F} = 50 \text{ mA}$ 

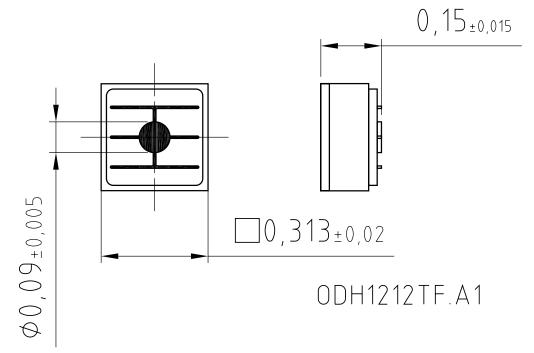


### Centroid Wavelength 7)





## Dimensional Drawing <sup>9)</sup>





### Disclaimer

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Language english will prevail in case of any discrepancies or deviations between the two language wordings.

#### Attention please!

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#### Glossary

- <sup>1)</sup> **Shelf life:** Temperature refer solely to storage of finished LED product (Not valid for chip on die sheet).
- <sup>2)</sup> **Reverse Operation:** Reverse Operation of 10 hours is permissible in total. Continuous reverse operation is not allowed.
- <sup>3)</sup> **Wavelength:** The wavelength is measured at a current pulse of typically 10 ms and with an internal reproducibility of  $\pm$  1 nm (with a coverage factor of k = 3).
- <sup>4)</sup> **Forward Voltage:** The forward voltage is measured during a current pulse of typically 5 ms and with an internal reproducibility of ± 0.1 V (with a coverage factor of k = 3).
- <sup>5)</sup> **Brightness:** Brightness values are measured during a current pulse of typically 10 ms and with an internal reproducibility of  $\pm 8$  % (with a coverage factor of k = 3).
- <sup>6)</sup> **Correlation Factor:** The exemplary correlation factor (CF) was estimated by sample build of the chip in a reference package and describes the exemplary correlation between the chip brightness measured in arbitrary units (a.u.) and the brightness in a reference package: CF = I/Φ(package) / I(chip). This factor is purely given as an indication of possible package brightness values. It may vary for different package es due to influences of geometries, reflectivity/refractive index of package materials or other material properties.
- <sup>7)</sup> Typical Values: Due to the special conditions of the manufacturing processes of semiconductor devices, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.
- <sup>8)</sup> **Characteristic curve:** In the range where the line of the graph is broken, you must expect higher differences between single devices within one packing unit.
- <sup>9)</sup> **Tolerance of Measure:** Unless otherwise noted in drawing, tolerances are specified with ±0.1 and dimensions are specified in mm.

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